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Monderer, B.; Pacifici, G.; Zukowski, C.;
Communications, 1990. ICC 90, Including Supercomm Technical Sessions. SUPERCOMM/ICC '90. Conferer International Conference on
16-19 April 1990 Page(s):567 - 571 vol.2

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2. **Trace cache redundancy: red and blue traces**

Ramirez, A.; Larriba-Pey, J.L.; Valero, M.;
High-Performance Computer Architecture, 2000..HPCA-6. Proceedings. Sixth International Symposium on
8-12 Jan. 2000 Page(s):325 - 333

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3. **The impact of resource partitioning on SMT processors**

Raasch, S.E.; Reinhardt, S.K.;
Parallel Architectures and Compilation Techniques, 2003. PACT 2003. Proceedings. 12th International Confe
27 Sept.-1 Oct. 2003 Page(s):15 - 25

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